Dear Editorial Board Members,

Our manuscript titled "A 36 nW, 7 ppm/oC On-Chip Clock Source Platform for Near-Human-Body Temperature Applications" is presented in this submission. In this manuscript, we describe the chip implementation and analysis of a complete clock source platform that is based on the premise of a locking principle presented in "Shrivastava, A.; Calhoun, B.H. A 150nW, 5ppm/oC, 100kHz On-Chip clock source for ultra-low power SoCs. In Custom Integrated Circuits Conference (CICC), 2012 "  
  
The proposed fully on-chip clock source platform is such that any oscillator can be integrated into it. It enables power-performance/power-stability trade off in a seamless fashion that makes it suitable for a variety of ultra low power applications, which fit the scope of this journal. We designed novel components including a diode-based temperature-uncompensated oscillator, calibration scheme and digital controller to demonstrate multiple locking schemes. We also demonstrate a system that specifically targets ultra low power, human-body-temperature range applications such as skin patch sensors.  The demonstrated system has the lowest energy per cycle and power compared to prior on-chip oscillators in the kHz frequency range.

The following manuscript is not under consideration or published in another journal and it has not been submitted previously to MDPI journals. Please consider our manuscript for publication in the Journal of Low Power Electronics and Applications.

Thanks,  
Divya Akella

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